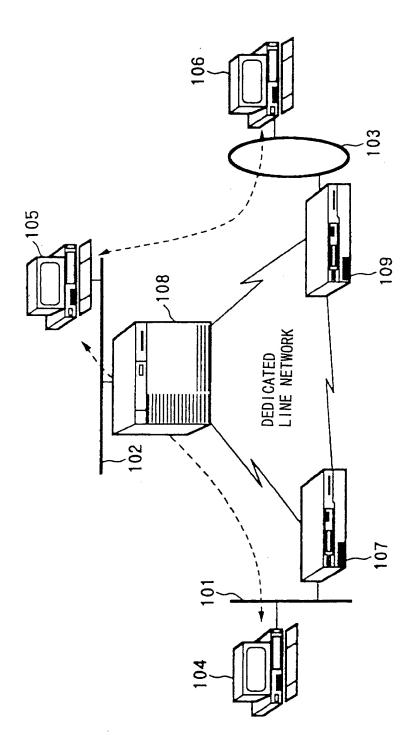
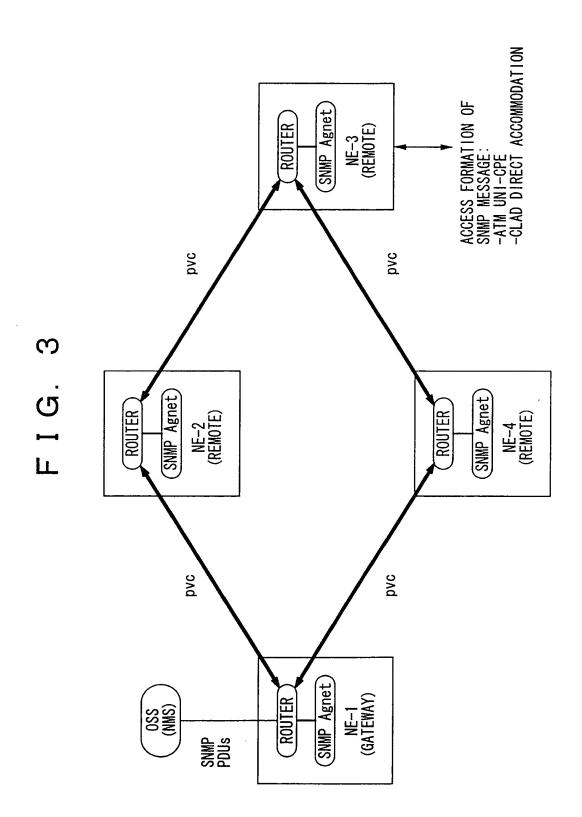
FIG.1

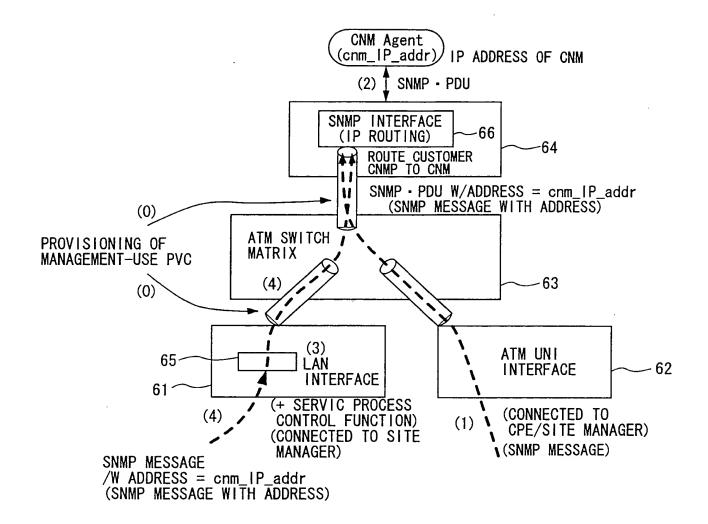


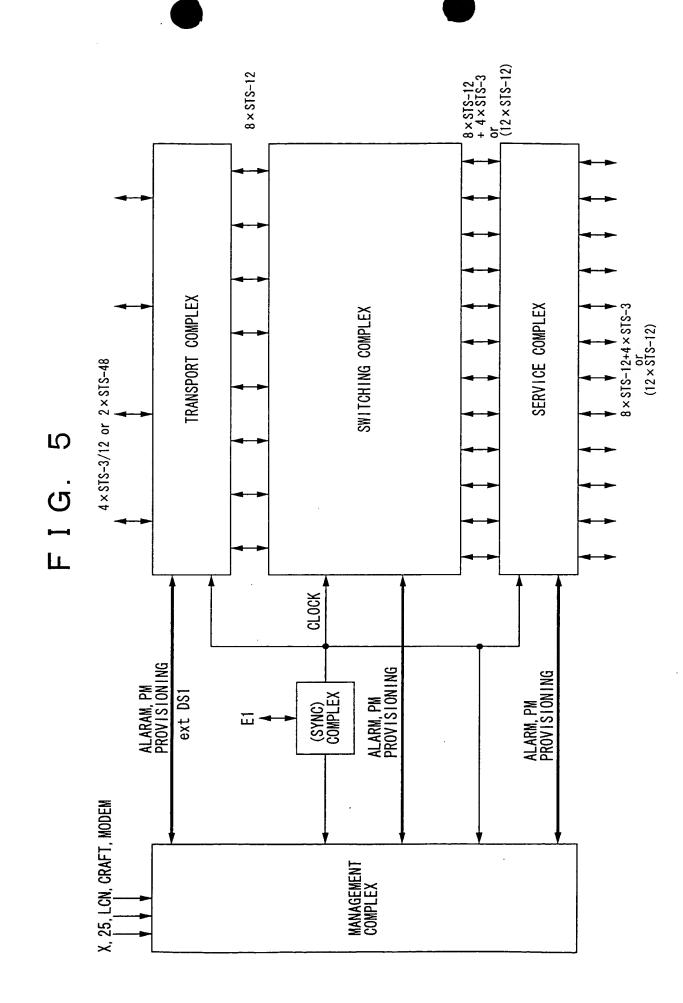
뮐 5 w/CLAD 2 (SPN) 之 (LAN CASE) SITE MANAGER (ATM UNI OR LAN SNMP  $\equiv$ a 음 <u>م</u> SNAP g <u>d</u> CPE 1(CPN) TAN INTERFACE/ USER LAN FACIL 14Y w/CLAD

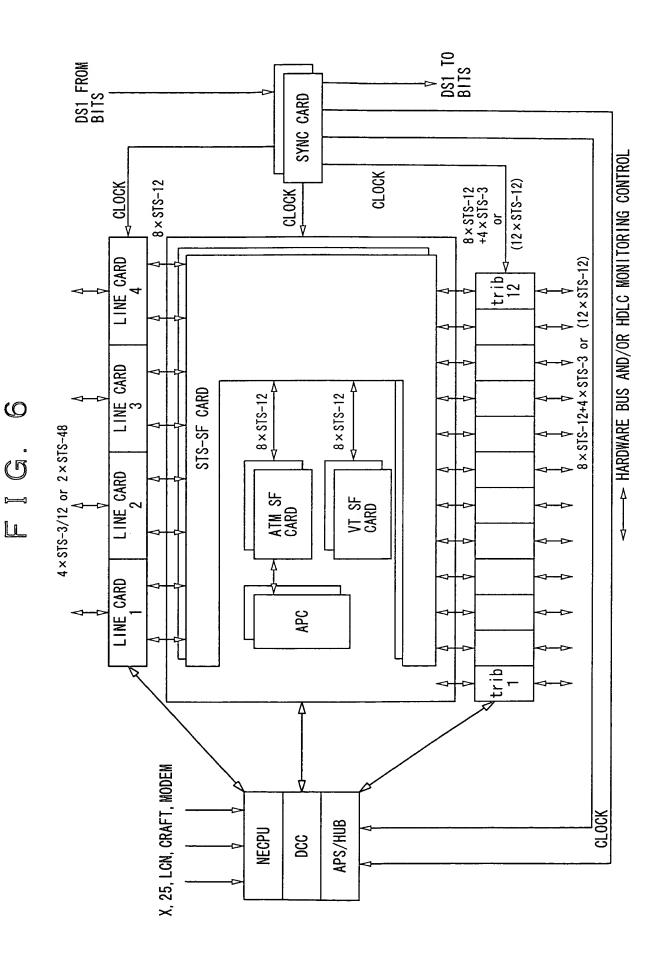
F I G.



F I G. 4







IDE NECPU NECPU 16SDCC SPC CARD STS-SF CARD VT SF CARD LINE CARD TRIB CARD SYNC CARD APC HARDWARE BUS APS/HUB

ATM-SF ATM-SF CPU BUS 윮 21 Adress Genaration PIF Geader Lines Remove det Gsync, syncik Gsync, syncik Remove det ABR Lines Subport 1/F Remove det Signaling DRVCPU DRVCPU Ca  $\subseteq$ 2 S APC Mate (Protection) APC (WORKING) (Rcv.) (Xmt.) HDLC Links 7 1 38. 7Mhz IN-BAND SNMP IN LOCAL HDLC LINK IN-BAND SNMP IN LOCAL HDLC LINK Hard bus 1/F 2e Stop ID JTAG Remove PWST, PWFL Port APS/HUB NECPU SYNC 200

00 . . .

(D)

Sub · Part Schedulers To ATM-SE To VT-SE COMMON MEMORY ADDRESS PIF HEADER Line CLOCK & GENERATION BLOCK FOR CPU & SWITCH To/from UIFs Schedulers DRAM MSC2 MSC1 REO To/from ATMSF,VTSF, anf mate APC GBK BUS LBEC 8 603e To/from ATMSF To/from APS/HUB HARDWARE BUS L2 CACHE 00100 SAR PF To/from APS/HUB DCC-M,NECPU, anf mate APC RS-232 LOCAL BUS PCI BUS PCI BRIDGE **DRIVER** HOLC